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9691 COMPUTING

9691/03

Paper 3 (Written Paper 3), maximum raw mark 90

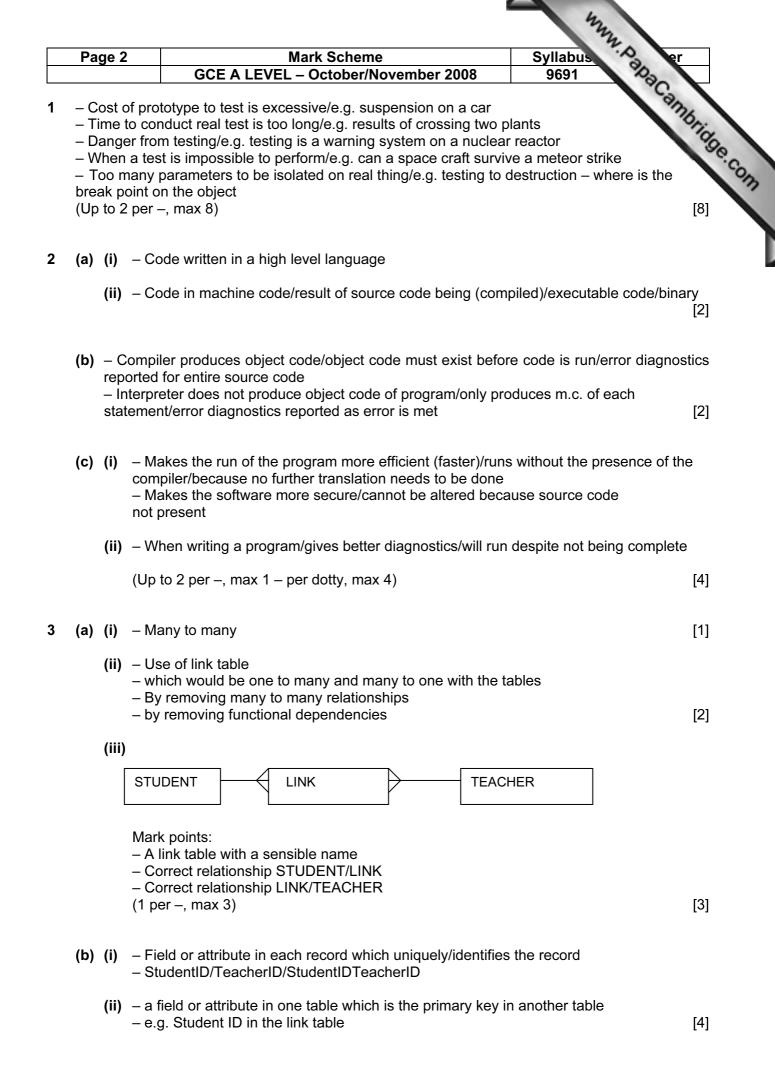
This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began.

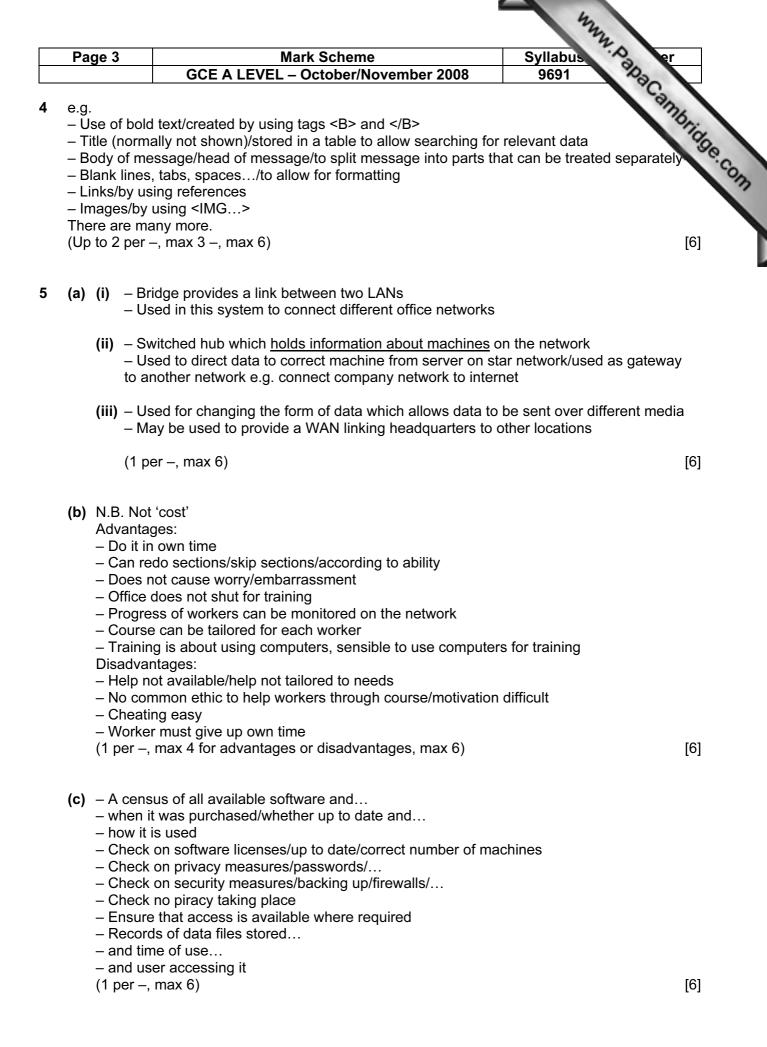
All Examiners are instructed that alternative correct answers and unexpected approaches in candidates' scripts must be given marks that fairly reflect the relevant knowledge and skills demonstrated.

Mark schemes must be read in conjunction with the question papers and the report on the examination.

CIE will not enter into discussions or correspondence in connection with these mark schemes.

CIE is publishing the mark schemes for the October/November 2008 question papers for most IGCSE, GCE Advanced Level and Advanced Subsidiary Level syllabuses and some Ordinary Level syllabuses.





Page 4	Mark Scheme	Syllabus er
•	GCE A LEVEL – October/November 2008	9691
-	 Memory divided into fixed sized units IAS is organised into physical pages Jobs/Data are divided into page sized pieces O.S. keeps an index of which jobs/data are in which pages Pages requiring processing need to be in memory Address may be in form of page and distance from start of 	
(ii) - - -	 Jobs/Data are divided up into logical amounts each of which is of a different size Memory tends to become fractured leading to compaction of memory being necessary Address complicated by need to calculate from start of seg 	
-	 Used when not enough space in memory Part of backing store used as though it were memory Contents must be copied to memory to be used Previous contents must be saved first Too much use of virtual memory leads to disk threshing 	
((1 per –, max 3 per dotty, max 9)	[9
(a) (i) /	A (self contained) set of instructions/piece of code/subprogra	am/subroutine
(ii)	nformation about a data item being given to a procedure wh	nen it is called [2
– Val – Wh – Re – Ca	turn address placed on stack lues of parameters placed on stack nen necessary, values of parameters are read from stack and turn address is read lling program carried out from return address	
(1 pe	er –, max 4)	[4
– Ins – Us – Fol	ncept of a stored program tructions and data use the same memory e of a single processor lows a linear sequence of instructions	
(1 pe	er –, max 3)	[3
	 Many (independent) processors working on the same program <u>at the same time</u> 	[2
-	 Weather forecasting requires large volume of calculations These can be speeded up because many can be done sim This means that more accurate forecasts are possible in restricted time frame Requires complex programs writing 	nultaneously
-	– Requires complex operating system (1 per –, max 4)	[4

