Electronics – 2019 June

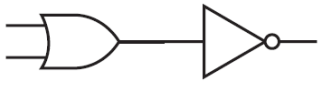
1. 0625/21/M/J/19/No.32

Which logic gate is represented by the symbol shown?

A AND **B** NAND **C** NOR **D** OR

2. 0625/21/M/J/19/No.33

The diagram shows a combination of logic gates.



Which single logic gate is equivalent to this combination?

A AND **B** NOR **C** NOT **D** OR

3. 0625/22/M/J/19/No.33

Which two logic gates each have a high output (1) when both of their inputs are low (0)?

A AND and OR

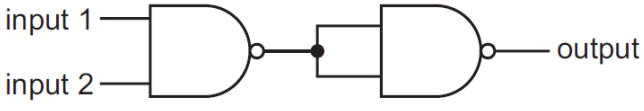
B AND and NOR

C NAND and NOR

D NAND and OR

4. 0625/22/M/J/19/No.34

Two NAND gates are joined together as shown.

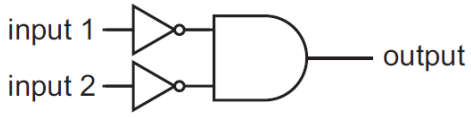


Which single logic gate is equivalent to this combination?

A AND **B** NAND **C** NOR **D** OR

5. 0625/23/M/J/19/No.34

The combination of logic gates shown has two inputs and one output.

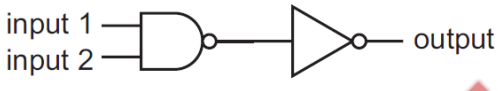


Which single logic gate is equivalent to this combination?

A AND **B** NOR **C** NOT **D** OR

6.

There are two inputs to the combination of logic gates shown, and one output.



Which truth table represents the action of this combination of gates?

A

input 1	input 2	output
0	0	0
0	1	0
1	0	0
1	1	1

B

input 1	input 2	output
0	0	0
0	1	1
1	0	1
1	1	1

C

input 1	input 2	output
0	0	1
0	1	1
1	0	1
1	1	0

D

input 1	input 2	output
0	0	1
0	1	0
1	0	0
1	1	0