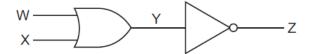
<u>Electronics – 2022 June IGCSE 0625</u>

1. June/2022/Paper_21/No.34

A digital circuit is made of two logic gates.

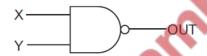


Which row is correct for this digital circuit?

	W	X	Y	Z
Α	0	0	1	0
В	0	1	0	1
С	1	0	1	0
D	1	1	0	1

2. June/2022/Paper_22/No.34

What is the truth table for the logic gate shown by the symbol?



X	Y	OUT		
0	0	0		
0	1	0		
1	0	0		
1	1	1		

В

X	Υ	OUT		
0	0	9		
0	1	1		
1	0	1		
1	1	1		

c

200	•	
X	Υ	OUT
0	0	1
0	1	0
1	0	0
1	1	0

D

X	Y	OUT
0	0	1
0	1	1
1	0	1
1	1	0

3. June/2022/Paper_23/No.34

The truth table for a logic gate is shown.

inp	output	
0	0	1
0	1	
1	0	1
1	0	

Which type of logic gate is it?

- A AND
- **B** NAND
- **C** OR
- **D** NOR

Con	nbinations of logic ga	ates are used w	vhen digital sig	nals are proces	ssed.	
(a)	Describe the difference diagram if it helps y		a digital signal	and an analog	ue signal. You ma	y include a
			•••••	•••••	<u></u>	
				40		[2]
(b)	Fig. 9.1 is the truth	table for a logic	gate X.	· All		
		input A	input B	output		
		0	0	1		
		0	(10	0		
		1	0	0		
		1	1	0		
		00	Fig. 9.1			
	State the name of lo	ogic gate X and	draw the sym	bol that repres	ents it.	
	name	Y				
	symbol					

4. June/2022/Paper_41/No.9

[1]

(c) Logic gate Y is identical to logic gate X.

Draw a combination of logic gates X and Y that behaves like an OR gate. Label the inputs A and B and label the output Q.

[2]

[Total: 5]

5. June/2022/Paper_42/No.10

Fig. 10.1 is a simplified diagram of a digital circuit. The output of logic gate Y controls a buzzer.

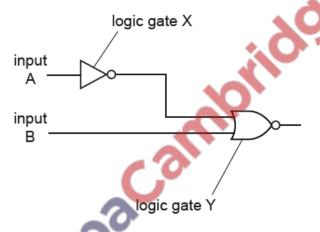


Fig. 10.1

(a) Complete Table 10.1, the truth table for the circuit.

Table 10.1

input A	input B	output of X	output of Y
0	0		
0	1		
1	0		
1	1		

[3]

(b)	Input A is the outplogic 0 when the h			hich gives logi	c 1 when the humid	ity is high and
	Input B is the outp The buzzer sound				bright light and logic	0 in darkness.
	State the condition	ns of humidity	and light wh	en the buzzer	is on.	
						[1]
(c)	The output of the	digital circuit a	alone is not a	able to operate	the buzzer.	
	Ring the compone circuit and the buz		ist that must	be connected	between the output	t of the digital
	fuse	heater	relay	resistor	thermistor	
	Explain your answ	/er.			0	
					<u> </u>	
				10)	Ž	[3]
				alujo.		[Total: 7]
			1			
			C			
			-0			
			Ö,			
		100	•			
	** 3					